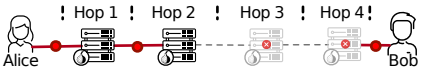
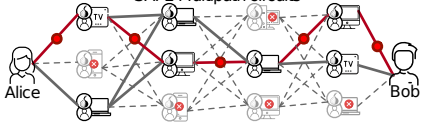


SAFE Multipath circuits



TOR Regular circuits